

AMENDMENTS TO THE SPECIFICATION

Please amend the specification by rewriting same to read as follows:

Page 19, please amend the paragraph commencing at line 18 by rewriting same to read as follows:

Fig. 2 is a block diagram showing an example of the audio reproducing apparatus 100 in concrete. An optical pickup 102 reads out data from a disk (optical disk) 101 and a reproduction processing section ~~303~~ 103 effects reproduction processing on the data thus read to provide reproduced digital audio data. This reproduced audio data is supplied to a digital-to-analog converter 104, in which it is converted into 2-channel analog audio signals. The analog audio signals thus converted are processed in an analog fashion such as an amplifying process by an analog processing section 105 and then outputted from analog output terminals (not shown) to the outside.

Page 22, please amend the paragraph commencing at line 7 by rewriting same to read as follows:

The audio processing at the audio amplifying apparatus 200 and the transmission processing through the interface section ~~209~~ 215 are executed under control of the central processing unit (CPU) 211. A memory 212 serving as a RAM for work area is connected to the CPU 211. Operating information from a button

213 disposed on an operating panel is supplied to the CPU 211 which controls operation corresponding to the operating information. Status such as the inputted signal selected state and the tone quality adjusted state may be displayed in the form of characters and graphic symbols by a display section 214 connected to the CPU 211. Further, when the interface section 209 215 receives data for controlling operation of this device through the IEEE 1394 system bus, the received data is supplied to the CPU 211 and the CPU 211 can control the corresponding operation. The controller 21 in the audio amplifying apparatus 200 shown in FIG 1. corresponds to the CPU 211, the audio output processing section 23 22 corresponds to the circuits such as the selecting section 201 and the signal processing section 203, and the transmission processing section 23 corresponds to the interface section 209 215.